

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Salman Akram

**Serial No.:** Not yet assigned

**Filed:** March 2, 2004

**For:** METHODS OF IMPROVING  
COPPER INTERCONNECTS OF  
SEMICONDUCTOR DEVICES FOR  
BONDING

**Examiner:** Unknown

**Group Art Unit:** Unknown

**Attorney Docket No.:** 2269-3854.3US  
(98-0854.03/US)

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**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The present application is a divisional of application Serial No. 10/383,042, filed March 6, 2003, pending, which is a divisional of application Serial No. 09/332,665, filed June 14, 1999, now U.S. Patent 6,544,880, issued April 8, 2003.

Pursuant to M.P.E.P. 2001.06(b), the Examiner is respectfully requested to consider the information of record in the prior application, and to confirm in the first Office Action on the merits that such art has in fact been reviewed. A PTO-1449 or PTO/SB/08 form listing all of the information of record in the prior application is enclosed herewith.

**Atty. Docket No.: 3854.3US (98-0854.03/US)**

This Information Disclosure Statement is filed within three (3) months of the filing date of the above-identified application, and no certification pursuant to 37 C.F.R. § 1.97(c) or a fee pursuant to 37 C.F.R. 1.17(p) is required.

Respectfully submitted,



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Date: March 2, 2004  
JRD/sls:djp

Enclosures: Form PTO-1449 or PTO/SB/08  
Document in ProLaw

Form PTO-1449  <b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  <i>(Use several sheets if necessary)</i>			Docket Number (Optional) <b>3854.3US (98-0854.03/US)</b>		Application Number <b>Not yet assigned</b>	
			Applicant <b>Salman Akram</b>			
			Filing Date <b>March 2, 2004</b>		Group Art Unit <b>Unknown</b>	

  

<b>U.S. PATENT DOCUMENTS</b>						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	3,648,131	03/1972	Stuby			
	3761,782	09/1973	Youmans			
	4,074,342	02/1978	Honn et al.			
	4,182,781	01/1980	Hooper et al.			
	4,360,142	11/1982	Carpenter et al.			
	4,670,770	06/1987	Tai			
	4,709,468	12/1987	Wilson			
	4,749,631	06/1988	Haluska et al.			
	4,750,666	06/1988	Neugebauer et al.			
	4,754,316	06/1988	Reid			
	4,756,977	07/1988	Butt et al.			
	4,821,151	04/1989	Pryor et al.			
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	5,106,461	04/1992	Volfson et al.			
	5,128,737	07/1992	van der Have			

  

<b>OTHER DOCUMENTS</b>		(Including Author, Title, Date, Pertinent Pages, Etc.)
		Stanley Wolf, Ph. D. and Richard N. Tauber, Ph. D., <i>Silicon Processing for the VLSI Era</i> , Volume 1: Process Technology, Pages 529-534.

  

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.



Form PTO-1449  <b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  <i>(Use several sheets if necessary)</i>				Docket Number (Optional) <b>3854.3US (98-0854.03/US)</b>		Application Number <b>Not yet assigned</b>	
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<b>U.S. PATENT DOCUMENTS</b>						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,470,787	11/1995	Greer			
	5,478,007	12/1995	Marrs			
	5,480,835	01/1996	Carney et al.			
	5,481,135	01/1996	Chandra et al.			
	5,485,038	01/1996	Licari et al.			
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	5,489,804	02/1996	Pasch			
	5,491,612	02/1996	Nicewarner, Jr.			
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	5,493,437	02/1996	Lebby et al.			
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	5,497,033	03/1996	Fillion et al.			
	5,502,002	03/1996	Wong et al.			
	5,503,286	04/1996	Nye, III et al.			
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	5,670,826	09/1997	Bessho et al.			

  

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